Application No.: 09/994,039

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application.

**Listing of Claims:** 

Claim 1 (Currently Amended): A method of driving a liquid crystal display, comprising:

modulating a first set of source data and supplying the modulated source data to a display

panel at an initial period of one frame interval, wherein modulating the source data includes

selecting a gray scale voltage level corresponding to the source data;

delaying the first-set of source data while supplying the modulated source data to the

display panel; and

applying a black voltage as black data to the display panel for at least a portion of the rest

period of the frame interval, the black voltage allowing a black picture to be displayed on the

display panel.

Claim 2 (Previously Presented): The method according to claim 1, further comprising

applying the delayed source data to the display panel in such a manner that the delayed source

data are positioned between the modulated source data and the black data within the frame

interval.

Claim 3 (Currently Amended): The method according to claim 1, wherein the modulated

modulating the source data includes selecting a grey scale voltage based on the include most

significant bits of the source data.

2

DC:50371749.1

Docket No.: 8734.024.00-US

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

Claim 4 (Currently Amended): The method according to claim 1, wherein the modulated

modulating the source data includes selecting a grey scale voltage based on all of the include

entire bits of the source data.

Claim 5 (Original): The method according to claim 1, further comprising alternatively

switching the modulated source data and the black data to apply to the display panel.

Claim 6 (Previously Presented): The method according to claim 1, further comprising

sequentially switching the modulated source data, the delayed source data, and the black data to

apply to the display panel.

Claim 7 (Previously Presented): The method according to claim 1, further comprising

delaying the source data during applying the modulated source data and the black data to the

display panel.

Claim 8 (Currently Amended): An apparatus for driving a liquid crystal display,

comprising:

a modulator modulating a first set of source data and supplying the modulated source data

to a display panel at an initial period of one frame interval, wherein the modulator includes a

look-up table;

a delay circuit delaying the first set of source data while the modulator supplying the

modulated source data to the display panel; and

3

DC:50371749.1

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

a black voltage generator generating a black voltage as black data to apply to the display

panel for at least a portion of the rest period of the one frame interval, the black voltage allowing

a black picture to be displayed on the display panel.

Claim 9 (Previously Presented): The apparatus according to claim 8, further comprising

a source data provider providing the delayed source data to the display panel in such a manner

that the source data are positioned between the modulated source data and the black data.

Claim 10 (Original): The apparatus according to claim 8, wherein the modulator

modulates most significant bits of the source data.

Claim 11 (Currently Amended): The apparatus according to claim 8, wherein the

modulator modulates all of the entire bits of the source data.

Claim 12 (Original): The apparatus according to claim 8, further comprising a switch

alternatively switching the modulated source data and the black data to apply to the display

panel.

Claim 13 (Previously Presented): The apparatus according to claim 8, further comprising

a switch sequentially switching the modulated source data, the delayed source data, and the black

data to apply to the display panel within the one frame interval.

4

DC:50371749.1

Application No.: 09/994,039

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

Docket No.: 8734.024.00-US

Claim 14 (Previously Presented): The apparatus according to claim 8, wherein the delay

circuit delaying the source data while the modulated source data and the black data are applied to

the display panel.

Claim 15 (Previously Presented): The apparatus according to claim 12, further

comprising:

a data driver applying the modulated source data and the black data from the switch to the

display panel;

a scanning driver applying a scanning signal to the display panel; and

a timing controller applying the source data to the modulator, and controlling the data

driver, the scanning driver, and a switching time of the switch.

Claim 16 (Currently Amended): The apparatus according to claim [[13]] 12, further

comprising:

a data driver applying the modulated source data, the delayed source data, and the black

data from the switch to the display panel;

a scanning driver applying a scanning signal to the display panel; and

a timing controller applying the source data to the modulator and the delay circuit, and

controlling the data driver, the scanning driver, and a switching time of the switch.

Claim 17 (Original): The apparatus according to claim 12, wherein the black data are

applied at about ½ of the one frame interval.

5

DC:50371749.1

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

Claim 18 (Previously Presented): The apparatus according to claim 13, wherein the modulated source data, the delayed source data and the black data are applied at about 1/3, 1/3 and 1/3 of the one frame interval, respectively.

Claim 19 (Currently Amended): A liquid crystal display comprising:

a liquid crystal display panel displaying images;

a data modulator modulating a first set of source data and supplying the modulated source data to the liquid crystal display at an initial period of one frame interval, wherein the data modulator selects a gray scale voltage level corresponding to the source data;

a delay circuit for delaying the first set of source data while the data modulator supplying the modulated source data to the liquid crystal display;

a black voltage generator generating a black voltage as black data allowing a black picture on the display panel at least for a portion of the rest period of the one frame interval;

a switch switching at least the modulated source data and the black data;

a data driver applying the modulated source data and the black data from the switch to the liquid crystal display panel;

a scanning driver applying scanning signal to the liquid crystal display panel; and a timing controller applying the source data to the modulator and controlling the data driver, the scanning driver, and a switching time of the switch.

Amdt. dated November 22, 2005

Reply to Final Office Action dated August 24, 2005

Claim 20 (Previously Presented): The liquid crystal display according to claim 19, wherein the switch switches among the modulated source data, the delayed source data and the

black data, so that the delayed source data are applied between the modulated source data and the

black data within the one frame interval.